

2000 Topical Meeting On Silicon Monolithic Integrated Circuits In Rf Systems: Digest Of Papers

By George E. Ponchak

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Patent US7761983 - Method of assembling a wafer -

Extraction Technique," 2004 Topical Meeting on Silicon Monolithic Integrated Circuits in RF Systems, on Silicon Monolithic Integrated Circuits in RF

Dr. George E. Ponchak - ZoomInfo -

George E. Ponchak (S'82 - M'83 - SM'97) In 1997-1998 and in 2000 He founded the IEEE Topical Meeting on Silicon Monolithic Integrated Circuits in RF Systems

University of Stuttgart | Institute for -

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Publications of Rhonda R Franklin - University of -

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France) Symposium E on Light Emission from Silicon -

Proceedings of Symposium E on Light Emission from Silicon Monolithic Integrated Circuits in Rf Systems: Digest of Papers Topical Meeting on Silicon

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Conferences | David S. Ricketts -

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on Silicon Monolithic Integrated Circuits in RF Systems Topical Meeting on Silicon Monolithic Integrated Circuits in RF Systems, Digest of papers,

Patent US7723999 - Calibration structures for -

calibration structures for differential signal " 2004 Topical Meeting on Silicon Monolithic Integrated Circuits in RF Systems,

Patent US7233160 - Wafer probe - Google Patent -

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www.acq.osd.mil -

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Patent US7482823 - Shielded probe for testing a -

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Patent US7233160 - Wafer probe - Google Patents -

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Microwave noise of Si/Si/sub 0.6/Ge/sub 0.4/ -

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G. E. Ponchak, A. Margomenos, and L. P. B. Katehi, -

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Patent US7688097 - Wafer probe - Google Patent -

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George E. Ponchak, Alan N. Downey Silicon Monolithic Integrated Circuits in RF Systems 2000, Digest of Papers, 2000 Topical Meeting, 2000,

Patent US7456646 - Wafer probe - Google Patent -

This application is a continuation of U.S. patent Meeting on Silicon Monolithic Integrated Circuits in RF Topical Meeting on Silicon Monolithic

Patent US7443186 - On-wafer test structures for -

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Patent US7453276 - Probe for combined signals - -

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