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By George E. Ponchak

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Patent US7453276 - Probe for combined signals - -

Extraction Technique," 2004 Topical Meeting on Silicon Monolithic Integrated Circuits in RF Systems, on Silicon Monolithic Integrated Circuits in RF

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How to Write a Paper for Journals and Navigate George E. Ponchak He founded the IEEE Topical Meeting on Silicon Monolithic Integrated Circuits in RF

Quebec) International Summer Category Theory -

Proceedings of an International Summer Category Theory Meeting, Silicon Monolithic Integrated Circuits in Rf Systems: Digest of Papers Topical Meeting

Patent US7443186 - On-wafer test structures for -

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Proceedings of the PHYSOR 2000 - Cowhosting -

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Microwave noise of Si/Si/sub 0.6/Ge/sub 0.4/ -

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France) Symposium E on Light Emission from Silicon -

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Transmission lines fabricated with CMOS -

George E. Ponchak, Alan N. Downey Silicon Monolithic Integrated Circuits in RF Systems 2000, Digest of Papers, 2000 Topical Meeting, 2000,

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Patent US7233160 - Wafer probe - Google Patents -

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Patent US7723999 - Calibration structures for -

calibration structures for differential signal " 2004 Topical Meeting on Silicon Monolithic Integrated Circuits in RF Systems,

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Patent US7761983 - Method of assembling a wafer -

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Patent US7456646 - Wafer probe - Google Patent -

This application is a continuation of U.S. patent Meeting on Silicon Monolithic Integrated Circuits in RF Topical Meeting on Silicon Monolithic

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G. E. Ponchak, A. Margomenos, and L. P. B. Katehi, -

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