

ESD: Analog Circuits And Design

By Steven H. Voldman

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EOS: Devices, Circuits and Systems ESD: Analog Devices and Circuits

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ESD Design for Analog Circuits covers many challenging topics related to analog circuit design for both ESD device and ESD circuits at the network level.

Analog, ESD, and EOS - ESD: Analog Circuits and -

The chapter opens with a discussion of analog design principles associated with matching and design symmetry. It also discusses electrical overstress (EOS) and its

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Genre/Form: Electronic books: Additional Physical Format: Print version: Voldman, Steven H. ESD : Analog Circuits and Design. Hoboken : Wiley, 2014

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Short description A comprehensive and in-depth review of the field, ESD: Analog Circuits and Design provides a balanced overview of analog circuit design layout

Esd: Analog Circuits and Design (Hardcover) | The -

Dr. Steven H. Voldman received his B. S. in Engineering Science from the University of Buffalo (1979); M.S. EE (1981) and Electrical Engineer Degree (1982) from M.I.T

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Genre/Form: Electronic books: Additional Physical Format: Print version: Voldman, Steven H. ESD. Chichester, West Sussex : John Wiley & Sons Ltd., [2015]

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